

# Eduardo SANCHEZ

## Publications (1993-)

### Papers and Conference Proceedings

#### 2005

- A. Upegui, C. A. Peña-Reyes, E. Sanchez. An FPGA Platform for On-line Topology Exploration of Spiking Neural Networks. *Microprocessors and Microsystems*, pp. 211-223, Vol. 29, Issue 5 (June), 2005.
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- A. Upegui, R. Moeckel, E. Dittrich, A. Ijspeert, E. Sanchez. An FPGA Dynamically Reconfigurable Framework for Modular Robotics. *Proceedings of the 18<sup>th</sup> International Conference on Architecture of Computing Systems, ARCS'05*, pp. 83-89, Innsbruck (Austria), March, 2005.
- A. Upegui, E. Sanchez. Evolving Hardware by Dynamically Reconfiguring Xilinx FPGAs. *Proceedings of the International Conference on Evolvable Systems, ICES'05*, Barcelona (Spain), September, 2005.
- J. M. Moreno, Y. Thoma, E. Sanchez. POetic: A Prototyping Platform for Bio-inspired Hardware. *Proceedings of the International Conference on Evolvable Systems, ICES'05*, Barcelona (Spain), September, 2005.

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- J.-M. Moreno, Y. Thoma, E. Sanchez, O. Torres, G. Tempesti. Hardware Realization of a Bio-Inspired POetic Tissue. In R. S. Zebulum et al. (eds.), *Proceedings of the 2004 NASA/DoD Conference on Evolvable Hardware*, pp. 237-244, IEEE Computer Society, Los Alamitos, Calif., 2004.

- A. Upegui, C. A. Peña-Reyes, E. Sanchez. A Hardware Implementation of a Network of Functional Spiking Neurons with Hebbian Learning. In A. J. Ijspeert, D. Mange, M. Murata, S. Nishio (eds.), *BioADIT 2004, Lecture Notes in Computer Science*, Vol. 3141, pp. 233-243, Springer-Verlag, 2004.

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